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TITLE: INTER-TASK QUEUE CONTROLLING SYSTEM

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ABSTRACT:

PURPOSE: To cope with a fault by only recording and erasing a relevant QUEUE entry
in and from a check point file, by setting implementing order information
indicating the implementing order of QUEUE entries in a relevant task in each QUEUE
entry.

CONSTITUTION: When a system fault occurs, a CPU 41 first reads the content of a
check point file 46 in an external storage device 44 into a QUEUE entry pool 60.
Then the CPU 41 checks all QUEUE entries in the pool 60 and couples tasks by
setting next entry pointers in the order of serial numbers at every task ID.
Thereafter, the CPU 41 sets the location of a QUEUE entry 62 restored in the pool
60 in the first entry pointer of a control entry 61 in a queue control table 50 and
the location of another QUEUE entry 63 in the final entry pointer. Furthermore, the
CPU 41 sets the serial number 3 of the entry 63 in the serial number counter of the
control entry 51.